

FIG. 1

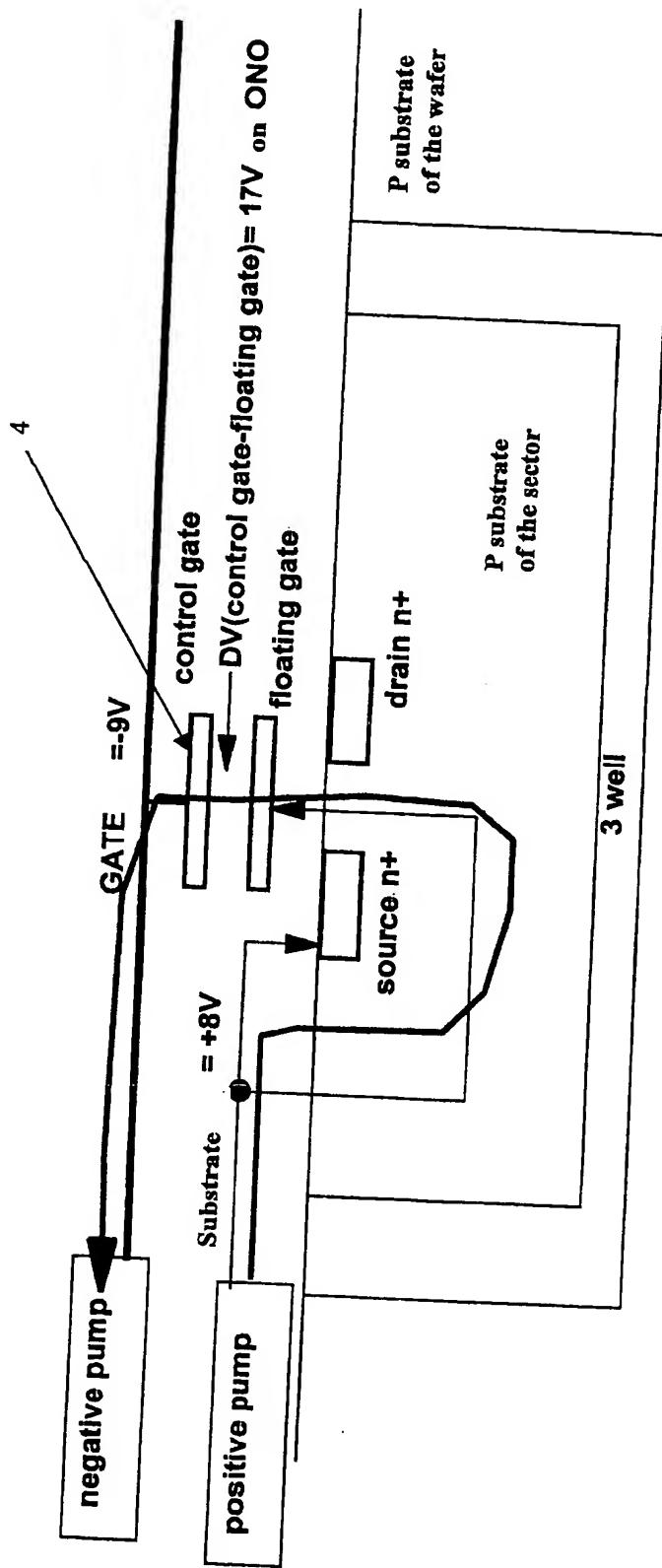


FIG. 2

Title: METHOD FOR DETECTING A RESISTIVE PATH OR A PREDETERMINED POTENTIAL
IN NON-VOLATILE MEMORY ELECTRONIC DEVICES
Inventor(s): Giovanni Campardo et al. Express Mail No. EV336610801US Docket No. 856063.746

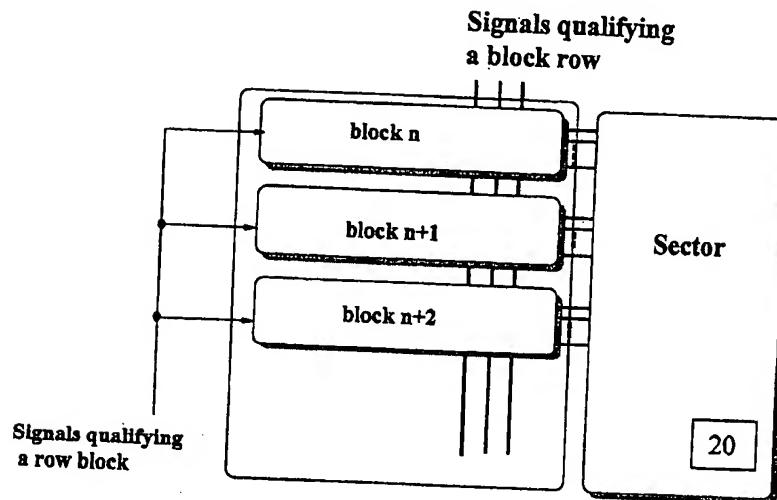


FIG. 3

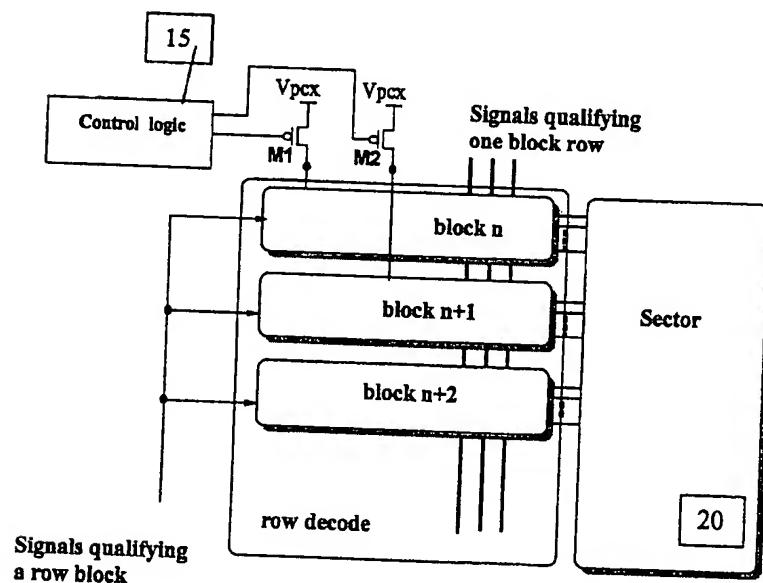
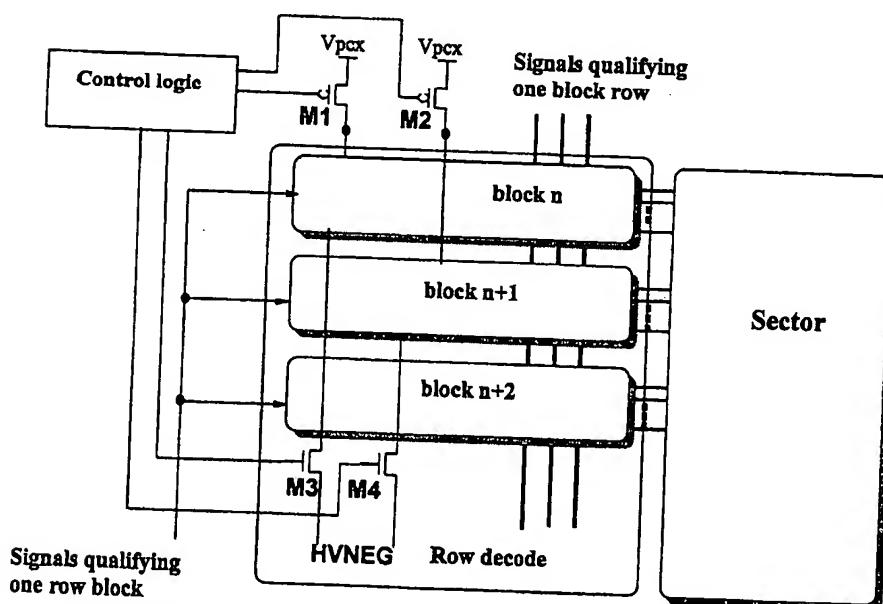
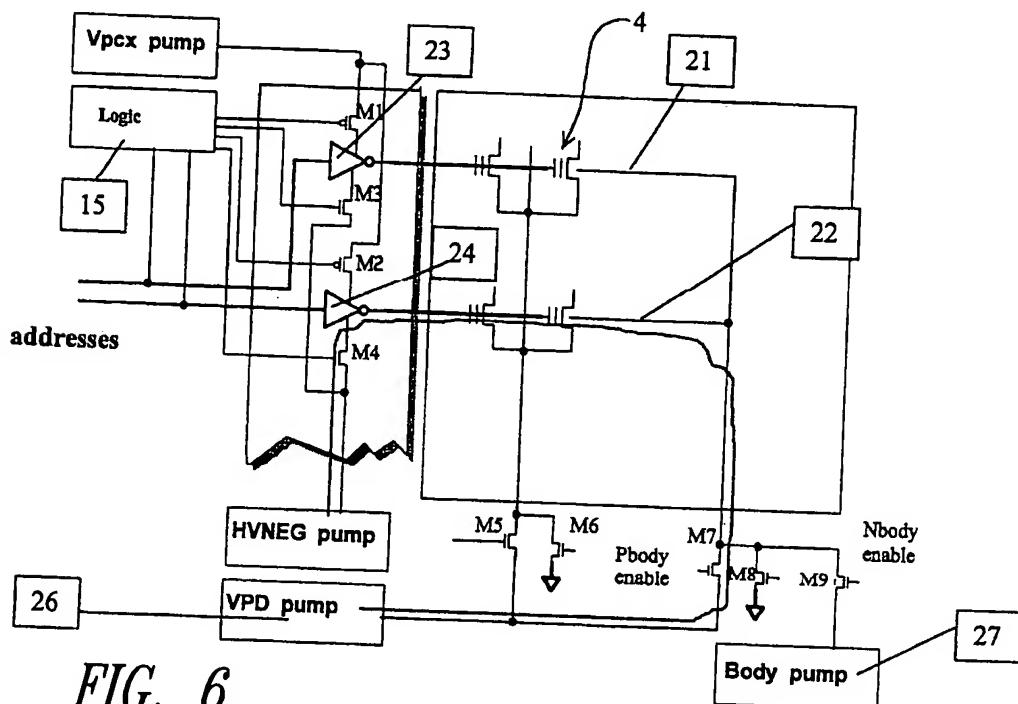


FIG. 4

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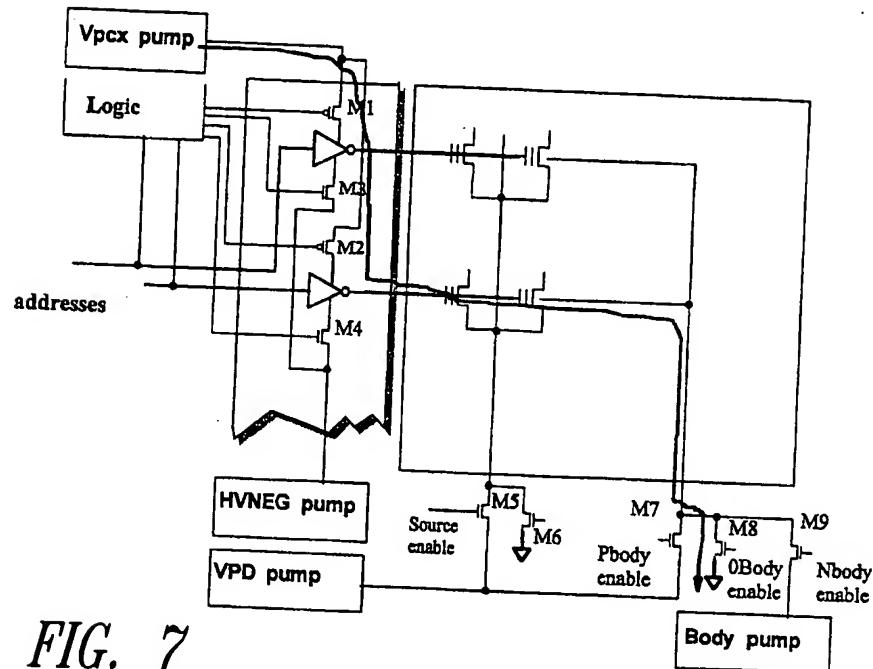


FIG. 7

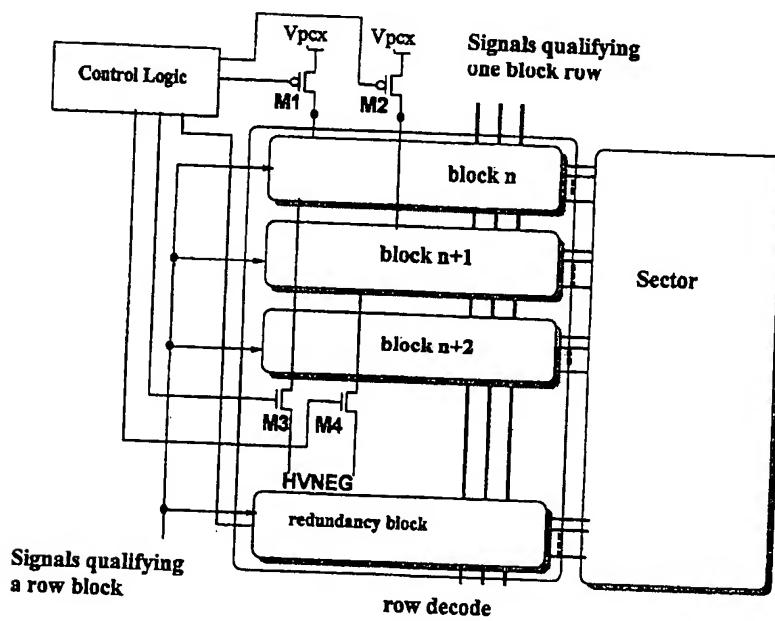
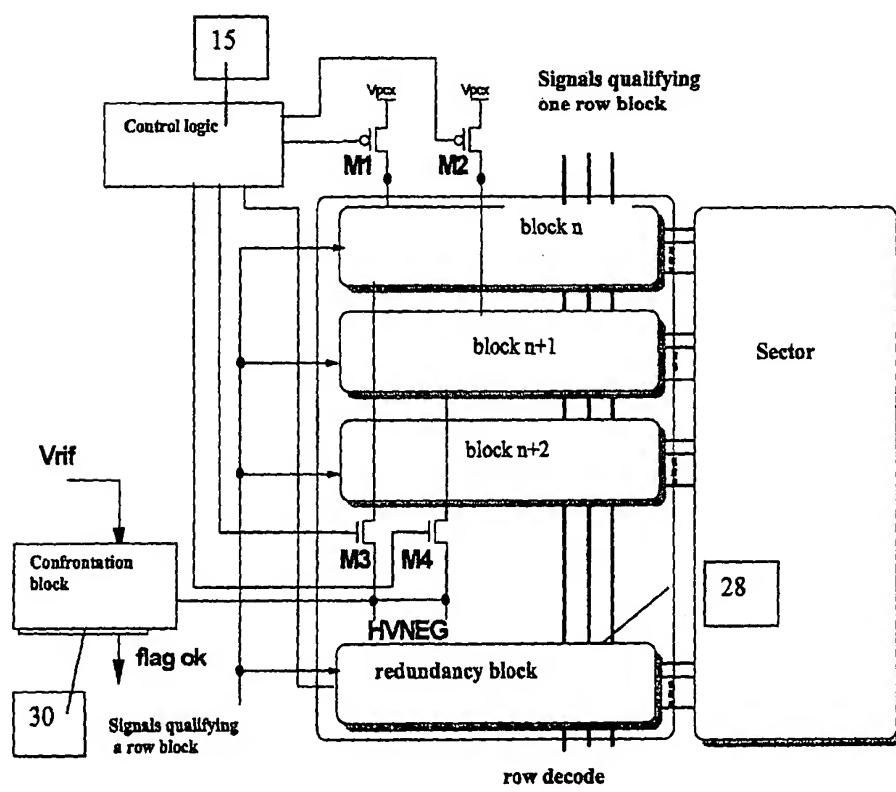
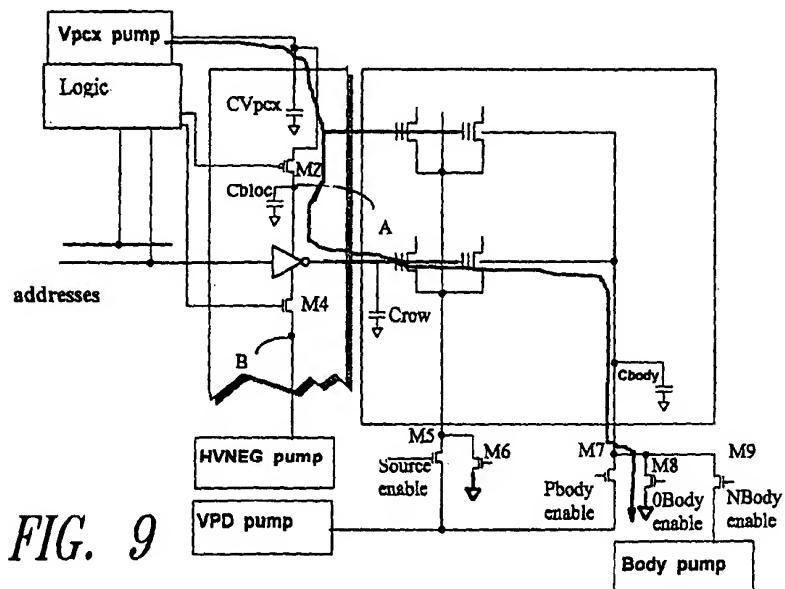


FIG. 8

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 Inventor(s): Giovanni Campardo et al. Express Mail No. EV336610801US Docket No. 856063.746



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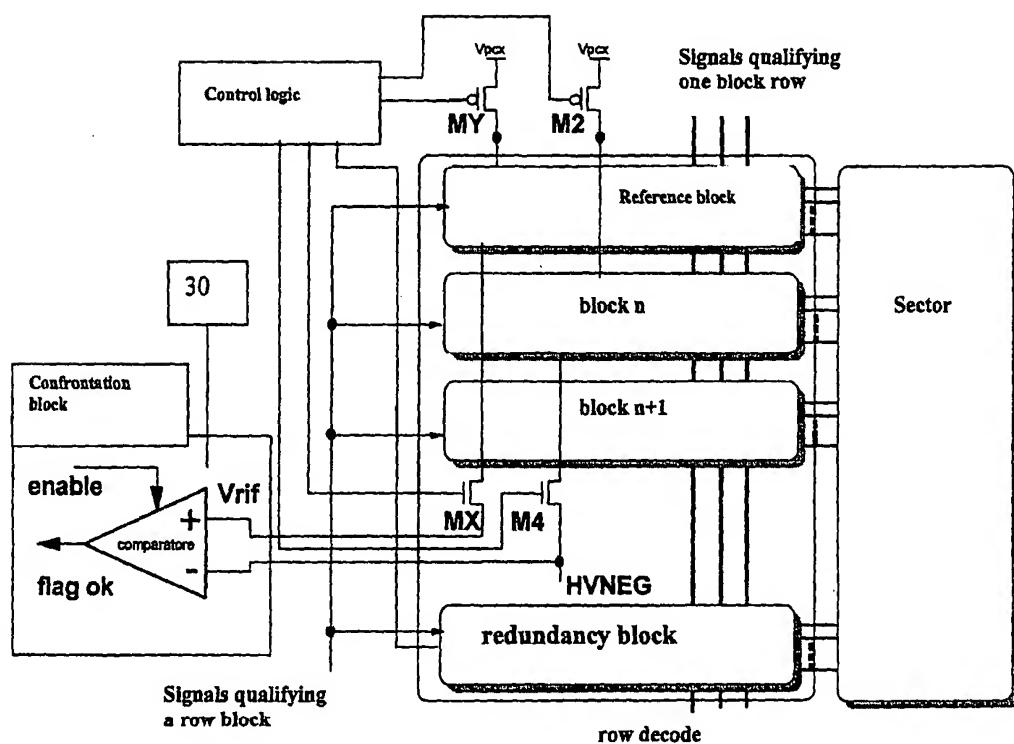


FIG. 11

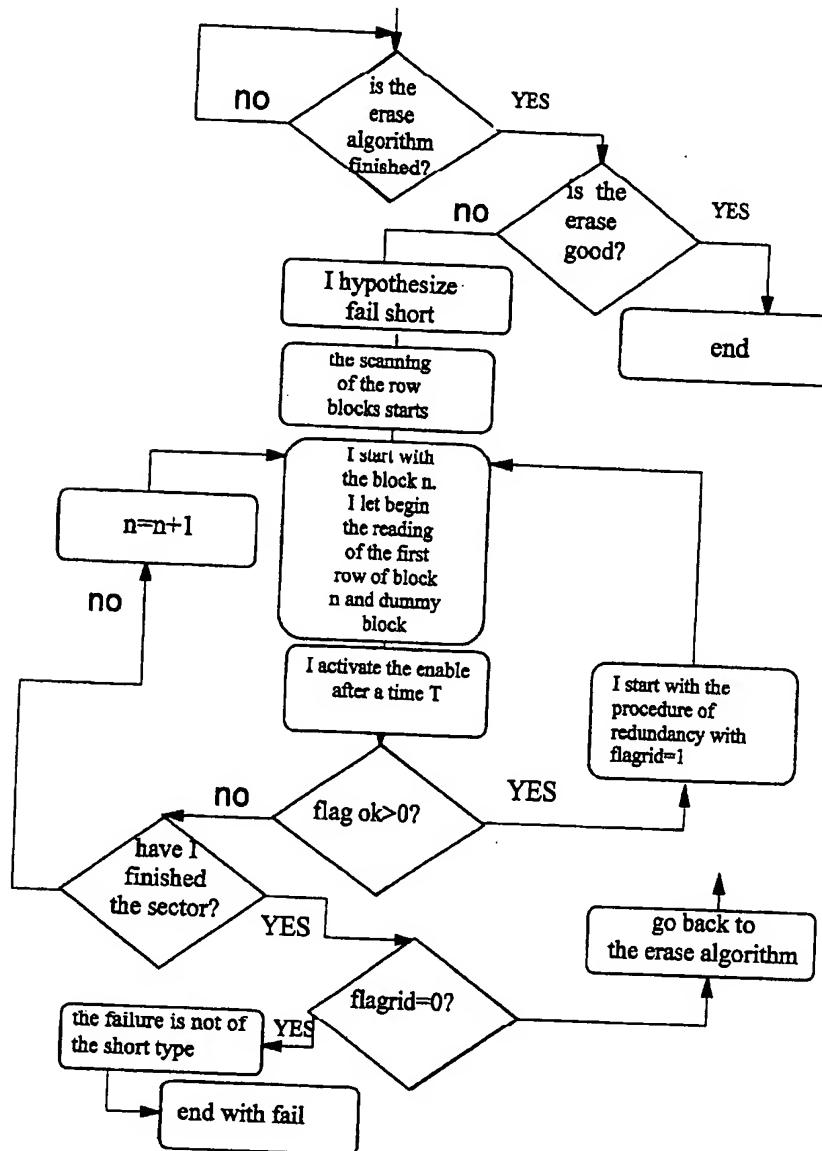


FIG. 12